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Method and apparatus for a coherence transformer with limited memory for connecting computer system ...

EE Hagersten, MD Hill, DA Wood, ... - US Patent 5,629,034, 1998 - freepatentsonline.com

... common bus 108 is a RTO (the first RTO of this transaction) and the current tag

is eS. coherence transformer 200 needs to invalidate any shared external copy ...

[Cited by 9](#) - [Related articles](#) - [Web Search](#) - [All 6 versions](#)
Multiprocessing system including an apparatus for optimizing spin-lock operations

EE Hagersten, ... - US Patent 5,660,159, 1999 - freepatentsonline.com

... agent 100 transmits a read to own coherency request ... 104 which are maintaining shared

copies of ... the requesting node indicating completion of the invalidation. ...

[Cited by 16](#) - [Related articles](#) - [Web Search](#) - [All 4 versions](#)
Multiprocessing system configured to perform efficient block copy operations

EE Hagersten, ... - US Patent App. 09/823,729, 2001 - Google Patents

... 1,2001 Sheet 12 of 20 US 2001/0037419 A1 192 194 196 Demand Code Demand Type Origin

RTS Read to Share H RTO Read to Own H RS Read Stream H INV Invalidate H ADM ...

[Cited by 22](#) - [Related articles](#) - [Web Search](#) - [All 6 versions](#)
Multiprocessing system configured to perform efficient write operations

EE Hagersten, ... - US Patent 5,745,085, 1998 - freepatentsonline.com

... agent 100 transmits a read to own coherency request ... 104 which are maintaining shared

copies of ... the requesting node indicating completion of the invalidation. ...

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A multiprocessing system configured to perform synchronization operations

EE Hagersten, RC Zak Jr, S Yang, A Guzovsky, WA, ... - EP Patent 0,817,075, 1998 - freepatentsonline.com

... agent 100 transmits a read to own coherency request ... 104 which are maintaining shared

copies of ... the requesting node indicating completion of the invalidation. ...

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Hybrid memory access protocol in a distributed shared memory computer system

EE Hagersten, MD Hill - US Patent 6,406,654, 2002 - freepatentsonline.com

... said memory access request is a read-to-own request and said first node possesses

a shared copy of said memory block; conveying an invalidate command to said ...

[Cited by 3](#) - [Related articles](#) - [Web Search](#) - [All 2 versions](#)
Multiprocessing system employing a three-hop communication protocol

EE Hagersten, PN Lowenstein - EP Patent 0,617,074, 1996 - freepatentsonline.com

... request agent 100 has a shared copy and ... When home agent 102 receives the

read-to-own request, home ... Home agent 102 further sends invalidation demands (reference ...

[Cited by 10](#) - [Related articles](#) - [Web Search](#) - [All 5 versions](#)
Multiprocessing system using an access to a second memory space to initiate software controlled data ...

EE Hagersten, ... - US Patent 5,848,254, 1998 - freepatentsonline.com

... agent 100 transmits a read to own coherency request ... 104 which are maintaining shared

copies of ... the requesting node indicating completion of the invalidation. ...

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Multiprocessing system configured to store coherency state within multiple subnodes of a processing ...

EE Hagersten, ... - US Patent 5,878,256, 1999 - freepatentsonline.com

... agent 100 transmits a read to own coherency request ... 104 which are maintaining shared

copies of ... the requesting node indicating completion of the invalidation. ...

[Cited by 18](#) - [Related articles](#) - [Web Search](#) - [All 5 versions](#)
Multiprocessing system employing address switches to control mixed broadcast snooping and directory...

RE Cypher, A Singhal, ... - US Patent App. 10/601,402, 2003 - Google Patents

... When a subsystem having a shared copy of data observes a coherence request for

exclusive access to the block, its copy is typically invalidated. ...

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Key authors: [E Hagersten](#) - [P Lowenstein](#) - [A Singhal](#) - [Z Fbrahim](#) - [M Hill](#)Result Page: [Previous](#) [1](#) [2](#) [3](#) [4](#) [5](#) [6](#) [7](#) [8](#) [9](#) [10](#) [11](#) [Next](#)

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